

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Balaji Prabhakar,)
et al.)
) Group: 2662
Serial No.: Not yet assigned)
) Examiner: Thien D. Tran
Filed: concurrently herewith)
) Our Ref: B-3457DIV 619142-7
For: "SWITCHING SYSTEMS AND METHODS)
OF OPERATION OF SWITCHING)
SYSTEMS") Date: November 13, 2001

11011 U.S. PTO
09/991302
11/13/01

Box New Patent Application
Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

In accordance with the Applicants' duty to disclose information which may be material to the examination of this application, the undersigned respectfully requests that the documents cited by the Examiner and/or submitted by the Applicants in connection with U.S. Serial No. 09/071,085 (the parent of Applicants' subject application) be considered when the subject application is examined.

For the Examiner's convenience, Form PTO-1449 (modified) is enclosed herewith listing the documents cited by the Examiner in connection with U.S. Serial No. 09/071,085. A copy of the cited documents can be found in the parent application, U.S. Serial No. 09/071,085.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made (37 C.F.R. 1.97(g)), an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists.

The filing of this Information Disclosure Statement shall not be construed as an admission against interest in any manner. (Notice of January 9, 1992, 1135 O.G. 13-25, at 25.)

Information Disclosure Statement
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The person making this statement is the practitioner who signs below on the basis of information supplied by an individual associated with the filing and prosecution of this application (37 C.F.R. § 1.56(c)) and on the basis of information in the practitioner's file.

The Commissioner is authorized to charge any additional fees which may be required or credit overpayment to deposit account no. 12-0415. In accordance with 37 CFR 1.97(b)(1), no fees should be due because this IDS is being filed concurrently herewith.

Respectfully submitted,

A handwritten signature in black ink, appearing to be 'R. P. Berg', written in a cursive style.

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Enclosures: Form PTO-1449 (modified) (2 pages)

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Form PTO-1449 (Modified)	ATTY DOCKET NO. B-3457DIV 619142-7	U.S. SERIAL NO. Not yet assigned
LIST OF PATENTS AND PUBLICATIONS STATEMENT	APPLICANT Balaji Prabhakar, et al.	
	FILING DATE concurrently herewith	GROUP unknown

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE or 102(e) DATE IF APPROPRIATE
	5,157,654	10/92	Cisneros			
	5,168,492	12/92	Beshai, et al.			
	5,742,606	04/98	Iliadis, et al.			
	5,790,522	08/98	Fichou, et al.			
	5,905,725	05/99	Sindhu, et al.			
	5,550,815	08/96	Cloonan, et al.			

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Charny, A., et al., "Algorithms for Providing Bandwidth and Delay guarantees in Input-Buffered Crossbars with Speedup," <i>IEEE</i> , pp. 235-340 (1998).
	Anderson, T.E., "High-Speed Switch Scheduling for Local-Area Networks," <i>ACM Transactions on Computer Systems</i> , Vol. 11, No. 4, pp. 319-352 (1993).
	Krishna, P., et al., "On the Speedup Required for Work-Conserving Crossbar Switches," <i>IEEE Journal on Selected Areas in Communications</i> , Vol. 17, No. 6, pp. 1057-1066 (1999).
	McKeown, N., "The iSLIP Scheduling Algorithm for Input-Queued Switches," <i>IEEE/ACM Transactions on Networking</i> , Vol. 7, No. 2, pp. 188-201 (1999).
	Charny, A., "Providing QoS Guarantees in Input Buffered Crossbar Switches with Speedup," PhD Thesis, Massachusetts Inst. Of Tech. (1998).

	Chang, C., et al., "A Broadband Packet Switch Architecture with Input and Output Queueing," <i>IEEE Globecom</i> , Vol. 1, pp. 448-452 (1994).
	Chuang, S., et al., "Matching Output Queueing with a Combined Input Output Queued Switch," Stanford CSL-TR-98-758, 25 pages.
	Gale, D., et al., "College Admissions and the Stability of Marriage," <i>American Mathematical Monthly</i> , Vol. 69, pp. 9-15 (1962).
	Karol, M., et al., "Input Versus Output Queueing on a Space-Division Packet Switch," <i>IEEE Transactions on Communications</i> , Vol. COM-35, No. 12, pp. 1347-1356 (1987).
	Prabhakar, B., et al., "On the speedup required for combined input and output queued switching," pp. 1-13.
	McKeown, N., et al., "Achieving 100% Throughput in an Input-Queued Switch," <i>IEEE Infocom '96</i> , Vol. 1, pp. 296-302 (1996).
	McKeown, N., "Scheduling Algorithms for Input-Queued Cell Switches," PhD Thesis, University of California at Berkeley, 119 pages (1995).

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.